

Fig. 1

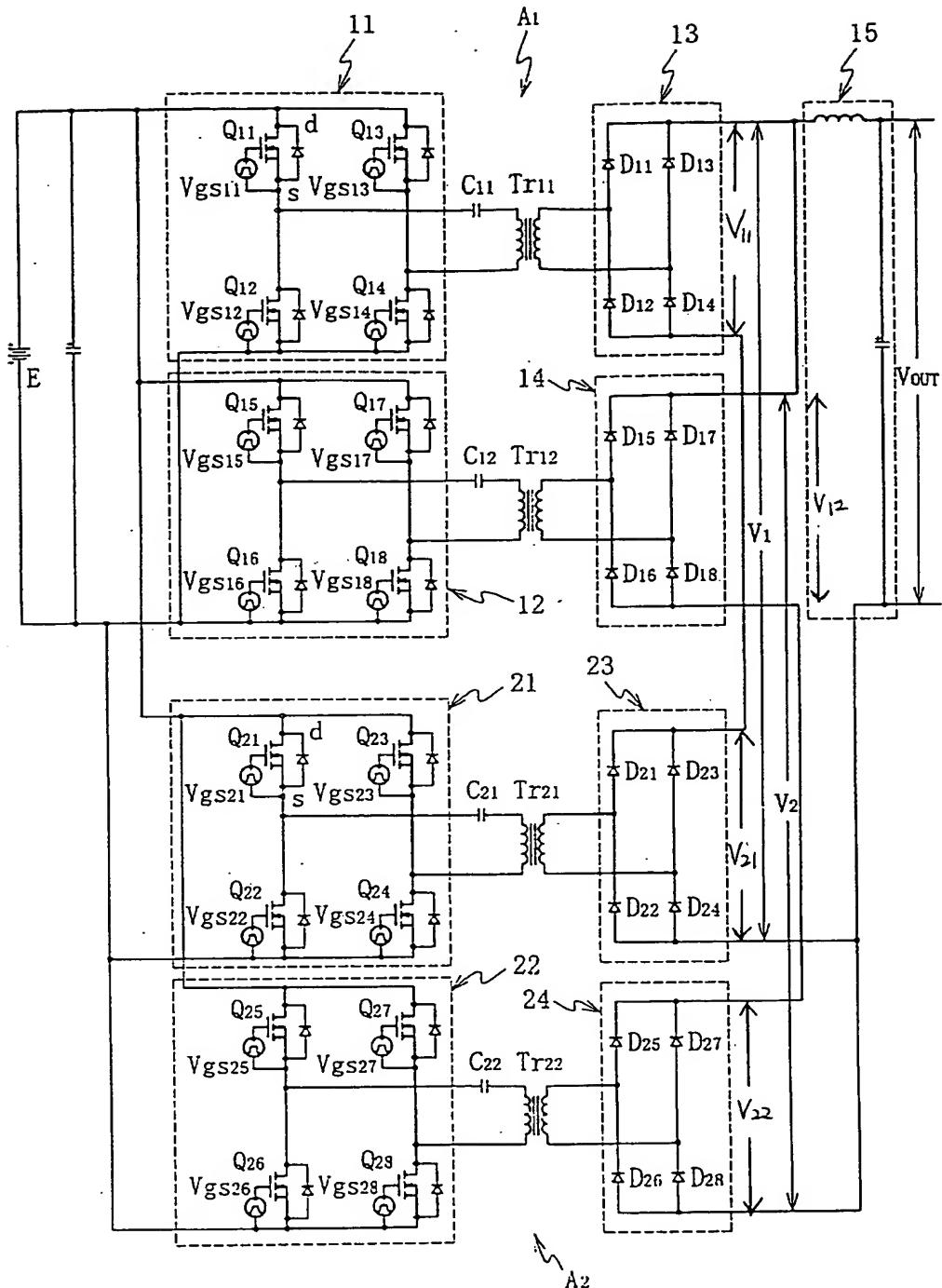
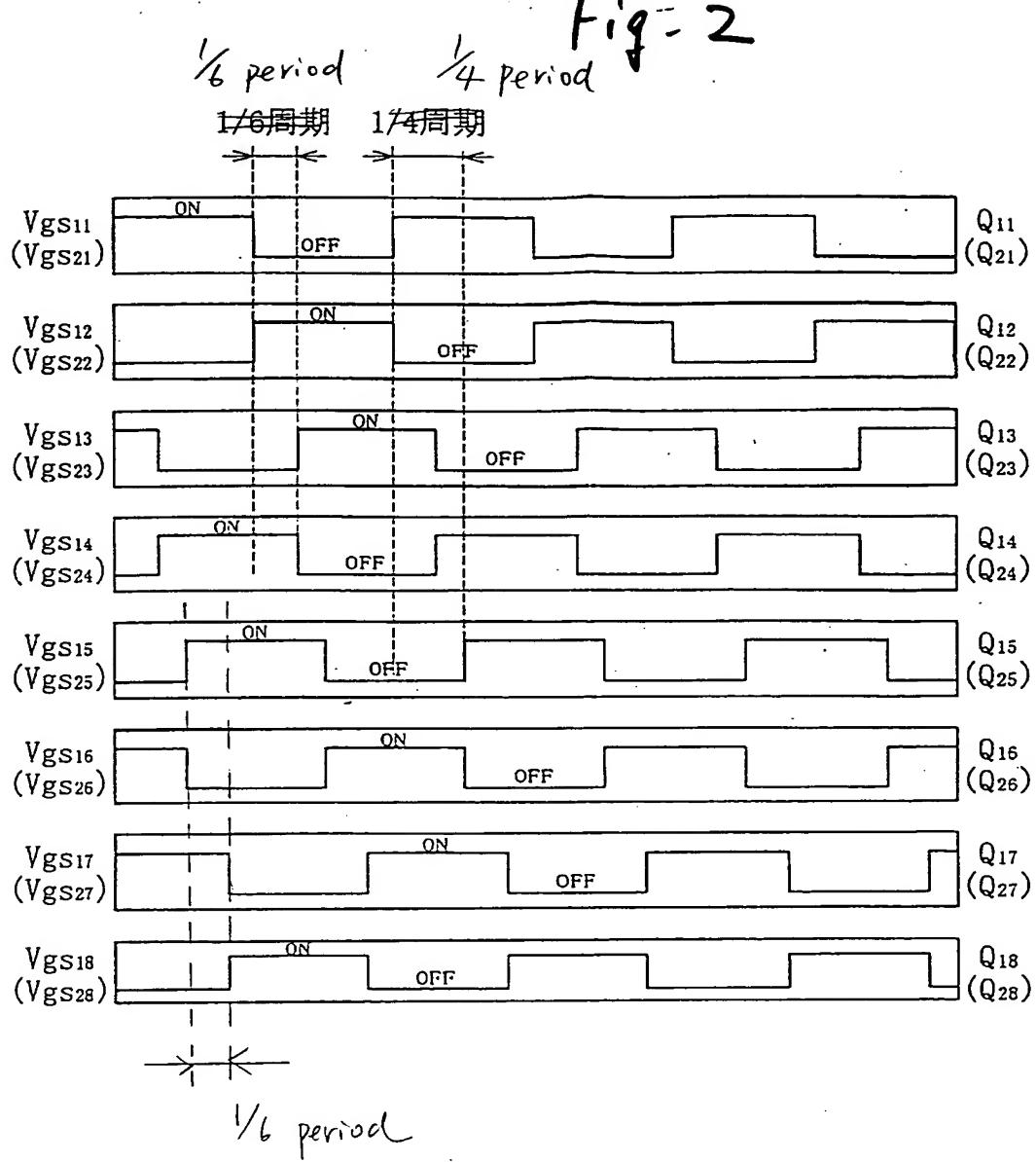


Fig-2



Commutation  
timing

Fig. 3

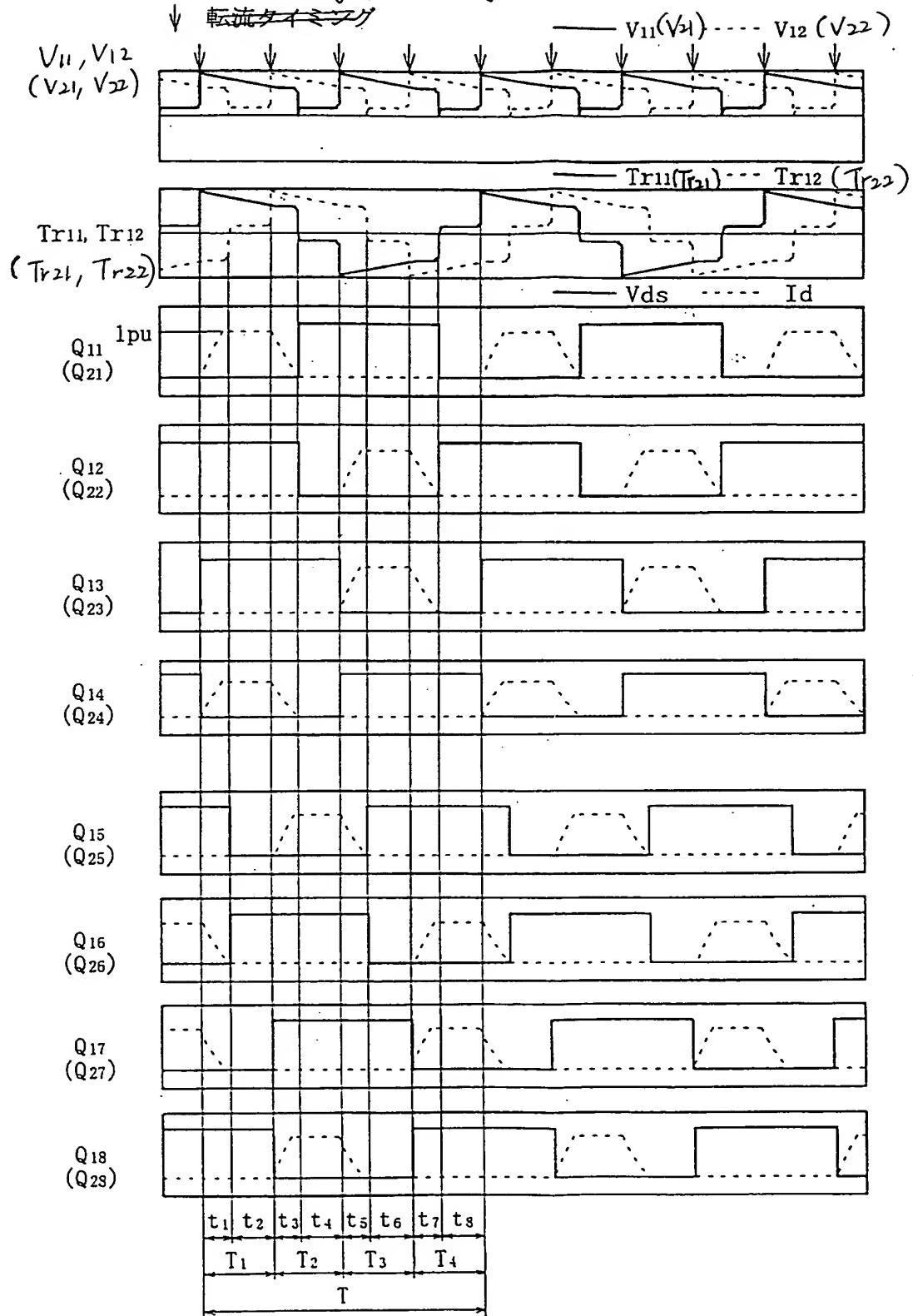


Fig. 4

		1周期 [T]					
		1/4周期 [1/4 · T]		1/4周期 [1/4 · T]		1/4周期 [1/4 · T]	
		t <sub>1</sub>	t <sub>2</sub>	t <sub>3</sub>	t <sub>4</sub>	t <sub>5</sub>	t <sub>6</sub>
Q <sub>11</sub> , Q <sub>14</sub> (Q <sub>21</sub> , Q <sub>24</sub> )	0 → 1	1 → 1	1 → 0	0 → 0	0 → 0	0 → 0	0 → 0
Q <sub>12</sub> , Q <sub>13</sub> (Q <sub>22</sub> , Q <sub>23</sub> )	0 → 0	0 → 0	0 → 0	0 → 0	0 → 1	1 → 1	1 → 0
Q <sub>15</sub> , Q <sub>18</sub> (Q <sub>25</sub> , Q <sub>28</sub> )	0 → 0	0 → 0	0 → 1	1 → 1	1 → 0	0 → 0	0 → 0
Q <sub>16</sub> , Q <sub>17</sub> (Q <sub>26</sub> , Q <sub>27</sub> )	1 → 0	0 → 0	0 → 0	0 → 0	0 → 0	0 → 0	1 → 1

Note

註： 0 → 1 indicates that current changes from 0 to 1 p.u.  
 1 → 1 indicates that it is maintained at current of 1.p.u.  
 1 → 0 indicates that current changes from 1.p.u. to 0.

Fig. 5

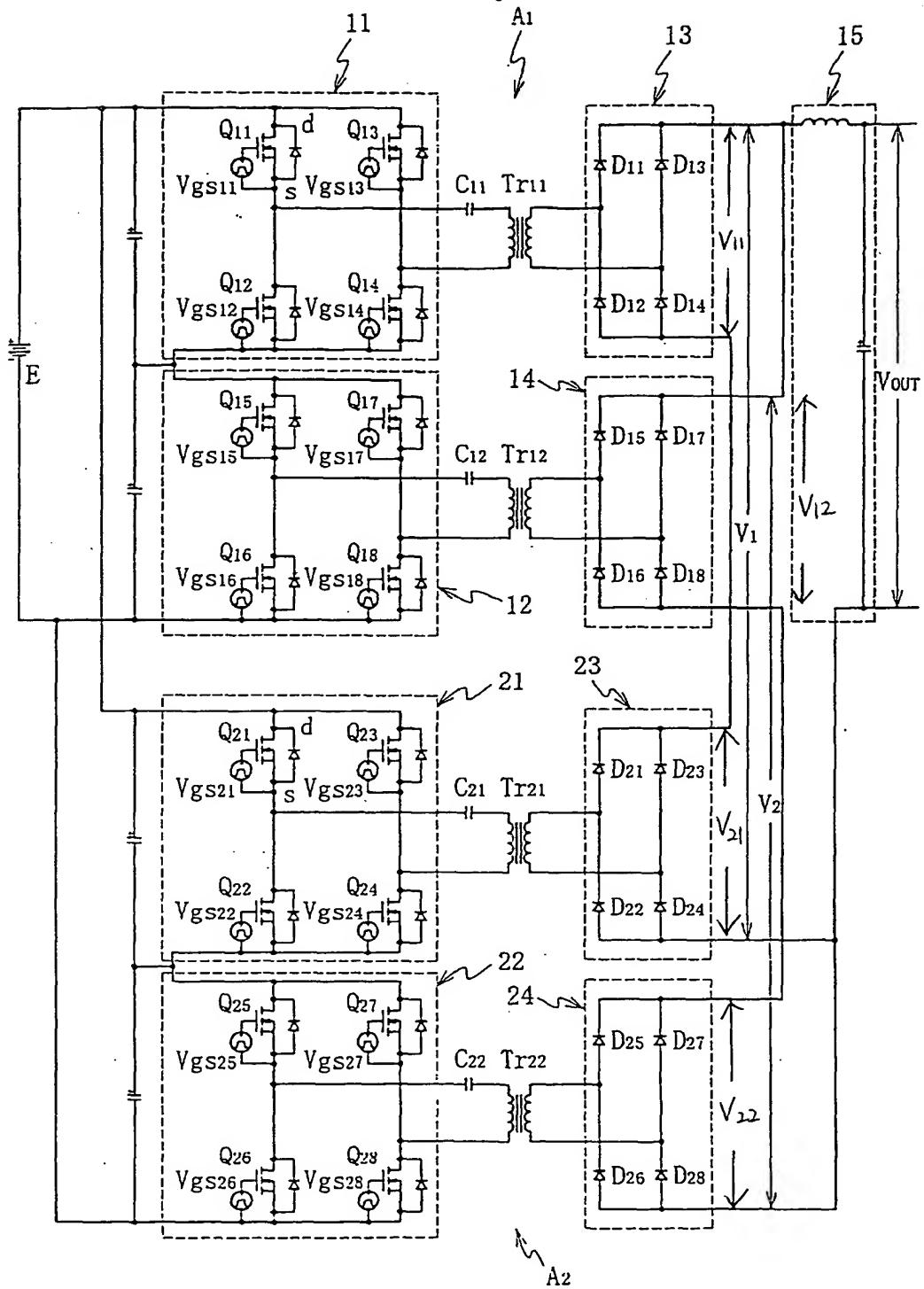


Fig. 6

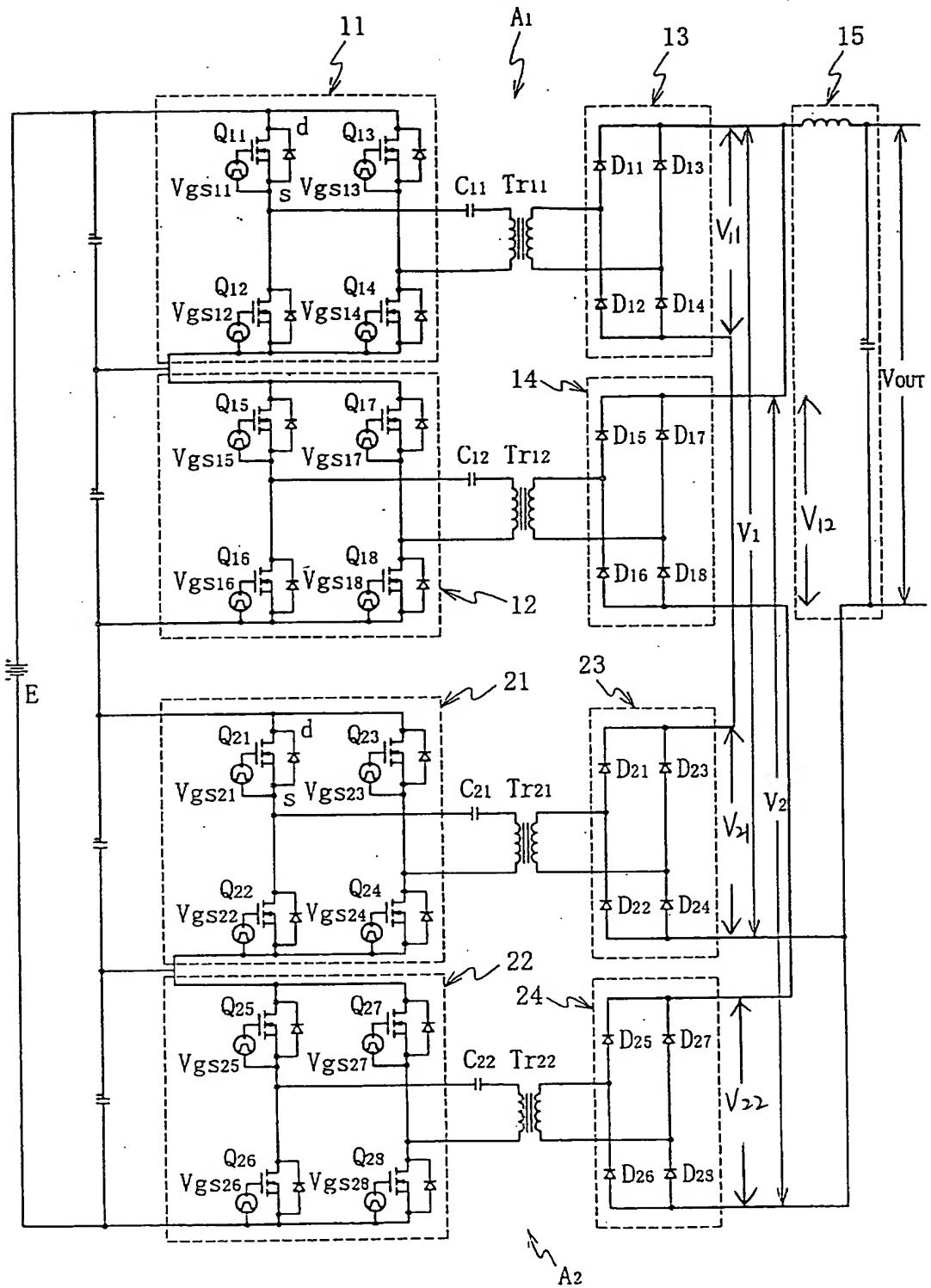


Fig. 7

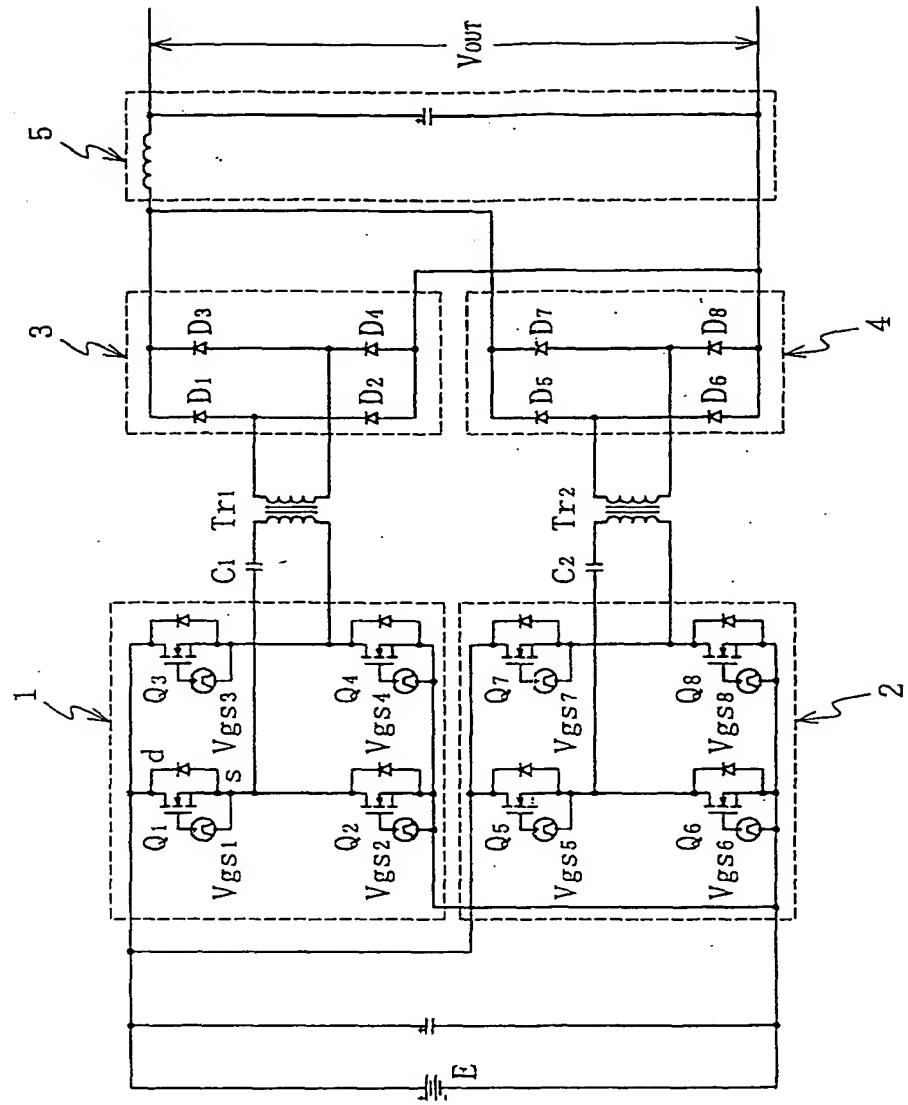


Fig. 8

